

[IEEE HOME](#) | [SEARCH IEEE](#) | [SHOP](#) | [WEB ACCOUNT](#) | [CONTACT IEEE](#)[Membership](#) [Publications/Services](#) [Standards](#) [Conferences](#) [Careers/Jobs](#)Welcome  
United States Patent and Trademark Of[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#) [Quick Links](#)

&gt; Se

Welcome to IEEE Xplore

- [Home](#)
- [What Can I Access?](#)
- [Log-out](#)

[Tables of Contents](#)

- [Journals & Magazines](#)
- [Conference Proceedings](#)
- [Standards](#)

[SEARCH](#)

- [By Author](#)
- [Basic](#)
- [Advanced](#)

[Member Services](#)

- [Join IEEE](#)
- [Establish IEEE Web Account](#)
- [Access the IEEE Member Digital Library](#)

[Print Format](#)Your search matched **27** of **945031** documents.

A maximum of **27** results are displayed, **25** to a page, sorted by **Relevance in descending** order. You may refine your search by editing the current search expression or entering a new one the text

Then click **Search Again**.

accelerator &lt;and&gt; coding

[Search Again](#)**Results:**Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD****1 A 1.2 billion operations per second video signal processing chip***Yates, R.; Evans, S.; Ivey, P.A.;*

Image Processing, 1994. Proceedings. ICIP-94., IEEE International Conference, Volume: 3 , 13-16 Nov. 1994

Page(s): 596 -600 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(344 KB\)\]](#) **IEEE CNF****2 Further improvements in TRACE 3-D***Rusthoi, D.P.; Lysenko, W.P.; Crandall, K.R.;*

Particle Accelerator Conference, 1997. Proceedings of the 1997 , Volume: 2 , 12 1997

Page(s): 2574 -2576 vol.2

[Print Format](#)[\[Abstract\]](#) [\[PDF Full-Text \(296 KB\)\]](#) **IEEE CNF****3 Cycle saving hardware for real-time audio processing***Sung Wook Park; Sun Kook Yoo; Nam Hun Jeong; Joon Seok Kim; Woo Suk Ko; Sup Lee; Dae Hee Youn;*

Electronics Letters , Volume: 34 Issue: 9 , 30 April 1998

Page(s): 847 -848

[\[Abstract\]](#) [\[PDF Full-Text \(252 KB\)\]](#) **IEE JNL****4 MPEG-2 video satellite transmission using ATM technology***Hower, S.M.;*

Broadcasting Convention, International (Conf. Publ. No. 428) , 12-16 Sept. 199

Page(s): 466 -470

[\[Abstract\]](#) [\[PDF Full-Text \(372 KB\)\]](#) **IEE CNF**

---

**5 Beam structure and transverse emittance studies of high-energy ion t**

*Saadatmand, K.; Johnson, K.F.; Schneider, J.D.;*

Particle Accelerator Conference, 1991. 'Accelerator Science and Technology'.,

Conference Record of the 1991 IEEE , 6-9 May 1991

Page(s): 1183 -1185 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(184 KB\)\]](#) **IEEE CNF**

---

**6 Two-klystron binary pulse compression at SLAC**

*Farkas, Z.D.; Lavine, T.L.; Menegat, A.; Vlieks, A.E.; Wang, J.W.; Wilson, P.B.;*

Particle Accelerator Conference, 1993., Proceedings of the 1993 , 17-20 May 19

Page(s): 1208 -1210 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(220 KB\)\]](#) **IEEE CNF**

---

**7 ATM and Internet via satellite**

*Chitre, P.;*

MILCOM 2000. 21st Century Military Communications Conference Proceedings ,

Volume: 2 , 22-25 Oct. 2000

Page(s): 624 -628 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(408 KB\)\]](#) **IEEE CNF**

---

**8 A high-speed pattern decoder in MPEG-4 padding block hardware acc**

*Hyeon-Cheol Mo; Jong-Sun Kim; Lee-Sup Kim;*

Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symposi

Volume: 2 , 6-9 May 2001

Page(s): 197 -200 vol. 2

[\[Abstract\]](#) [\[PDF Full-Text \(288 KB\)\]](#) **IEEE CNF**

---

**9 MPEG2 video decoder and AC-3 audio decoder LSIs for DVD player**

*Yasuda, M.; Nishio, T.; Toyokura, M.; Kawamura, A.; Fujiwara, M.; Akiyama, T.*

Consumer Electronics, IEEE Transactions on , Volume: 43 Issue: 3 , Aug. 1997

Page(s): 462 -468

[\[Abstract\]](#) [\[PDF Full-Text \(792 KB\)\]](#) **IEEE JNL**

---

**10 Keeping track of the big event***Lewin, D.I.;*

Computing in Science &amp; Engineering [see also IEEE Computational Science and Engineering] , Volume: 3 Issue: 5 , Sept.-Oct. 2001

Page(s): 8 -11

[\[Abstract\]](#) [\[PDF Full-Text \(296 KB\)\]](#) **IEEE JNL****11 An MPEG-2 codec LSI with an audio accelerator for camcorders***Nakamura, T.; Marumori, H.; Takahashi, M.; Fujii, Y.;*

Consumer Electronics, IEEE Transactions on , Volume: 48 Issue: 3 , Aug. 2002

Page(s): 656 -661

[\[Abstract\]](#) [\[PDF Full-Text \(597 KB\)\]](#) **IEEE JNL****12 A software image coding system***Cozens, S.; Ivey, P.A.; Myers, D.J.;*

Image Processing and its Applications, 1995., Fifth International Conference on Jul 1995

Page(s): 36 -39

[\[Abstract\]](#) [\[PDF Full-Text \(244 KB\)\]](#) **IEE CNF****13 A hardware implementation of gridless routing based on content addressable memory***Sato, M.; Kubota, K.; Ohtsuki, T.;*

Design Automation Conference, 1990. Proceedings. 27th ACM/IEEE , 24-28 June

Page(s): 646 -649

[\[Abstract\]](#) [\[PDF Full-Text \(340 KB\)\]](#) **IEEE CNF****14 Beyond-binary circuits for signal processing***Hanyu, T.; Kameyama, M.; Higuchi, T.;*

Solid-State Circuits Conference, 1993. Digest of Technical Papers. 40th ISSCC., IEEE International , 24-26 Feb. 1993

Page(s): 134 -135, 276-7

[\[Abstract\]](#) [\[PDF Full-Text \(724 KB\)\]](#) **IEEE CNF****15 Latest benchmark results of VHDL simulation systems***Rohm, E.;*

Design Automation Conference, 1995, with EURO-VHDL, Proceedings EURO-DA European , 18-22 Sept. 1995

Page(s): 406 -411

[\[Abstract\]](#) [\[PDF Full-Text \(440 KB\)\]](#) **IEEE CNF**

---

**16 On-chip hardware accelerator for model-based 3-D instrumentation using run-length matching**

*Kamoshida, M.; Hanyu, T.; Kameyama, M.;*

Industrial Electronics, Control, and Instrumentation, 1995., Proceedings of the IEEE IECON 21st International Conference on, Volume: 2, 6-10 Nov. 1995

Page(s): 1319 -1323 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(384 KB\)\]](#) **IEEE CNF**

---

**17 The Procedure Execution Manager and its application to Advanced Particle Source operation**

*Borland, M.;*

Particle Accelerator Conference, 1997. Proceedings of the 1997, Volume: 2, 12 Nov. 1997

Page(s): 2410 -2412 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(348 KB\)\]](#) **IEEE CNF**

---

**18 A microprocessor with a 128 b CPU, 10 floating-point MACs, 4 floating-point dividers, and an MPEG2 decoder**

*Kutaragi, K.; Suzuki, M.; Hiroi, T.; Magoshi, H.; Okamoto, S.; Oka, M.; Ohba, T.; Yamamoto, Y.; Furuhashi, M.; Tanaka, M.; Yutaka, T.; Okada, T.; Nagamatsu, I.; Urakawa, Y.; Funyu, M.; Kunimatsu, A.; Goto, H.; Hashimoto, K.; Ide, N.; Mura, H.; Ohtagu*

Solid-State Circuits Conference, 1999. Digest of Technical Papers. ISSCC. 1999 International, 15-17 Feb. 1999

Page(s): 256 -257

[\[Abstract\]](#) [\[PDF Full-Text \(164 KB\)\]](#) **IEEE CNF**

---

**19 Efficient transmission of triangle meshes to graphics processors**

*Chou, P.H.; Meng, T.H.;*

Signal Processing Systems, 2000. SiPS 2000. 2000 IEEE Workshop on, 11-13 Oct. 2000

Page(s): 275 -284

[\[Abstract\]](#) [\[PDF Full-Text \(468 KB\)\]](#) **IEEE CNF**

---

**20 Reconfigurable and programmable minimum distance search engine for portable video compression systems**

*Chang-Ki Kwon; Kwyro Lee;*

Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symposium

Volume: 4 , 6-9 May 2001

Page(s): 542 -545 vol. 4

[\[Abstract\]](#) [\[PDF Full-Text \(336 KB\)\]](#) **IEEE CNF**

---

**21 A low-cost media-processor based real-time MPEG-4 video decoder**

*Jin-Hau Kuo; Ja-Ling Wu; Jim Shiu; Kan-Li Huang;*

Consumer Electronics, 2002. ICCE. 2002 Digest of Technical Papers. International Conference on , 18-20 June 2002

Page(s): 272 -273

[\[Abstract\]](#) [\[PDF Full-Text \(298 KB\)\]](#) **IEEE CNF**

---

**22 An MPEG-2 CODEC LSI with an audio accelerator for camcorders**

*Nakamura, T.; Marumori, H.; Takahashi, M.; Fujii, Y.;*

Consumer Electronics, 2002. ICCE. 2002 Digest of Technical Papers. International Conference on , 18-20 June 2002

Page(s): 326 -327

[\[Abstract\]](#) [\[PDF Full-Text \(258 KB\)\]](#) **IEEE CNF**

---

**23 Design and VLSI implementation of a digital audio-specific DSP core MP3/AAC**

*Kyoung Ho Bang; Nam Hun Jeong; Joon Seok Kim; Young Cheol Park; Dae Hee*  
Consumer Electronics, 2002. ICCE. 2002 Digest of Technical Papers. International Conference on , 18-20 June 2002

Page(s): 220 -221

[\[Abstract\]](#) [\[PDF Full-Text \(327 KB\)\]](#) **IEEE CNF**

---

**24 Image coding/reconstruction and matching using a parallel distributed Hebbian architecture**

*Lam, K.P.;*

Neural Networks, 2002. IJCNN '02. Proceedings of the 2002 International Joint Conference on , Volume: 1 , 12-17 May 2002

Page(s): 974 -979

[\[Abstract\]](#) [\[PDF Full-Text \(398 KB\)\]](#) **IEEE CNF**

---

**25 A microprocessor with a 128-bit CPU, ten floating-point MAC's, four floating-point dividers, and an MPEG-2 decoder**

*Suzuoki, M.; Kutaragi, K.; Hiroi, T.; Magoshi, H.; Okamoto, S.; Oka, M.; Ohba, Y.; Yamamoto, Y.; Furuhashi, M.; Tanaka, M.; Yutaka, T.; Okada, T.; Nagamatsu, I; Urakawa, Y.; Funyu, M.; Kunimatsu, A.; Goto, H.; Hashimoto, K.; Ide, N.; Mura H.; Ohtagu*

Solid-State Circuits, IEEE Journal of, Volume: 34 Issue: 11, Nov. 1999  
Page(s): 1608 -1618

[\[Abstract\]](#) [\[PDF Full-Text \(640 KB\)\]](#) **IEEE JNL**

---

[1](#) [2](#) [\[Next\]](#)

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)  
[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#)  
[No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2003 IEEE — All rights reserved

# WEST Search History

DATE: Monday, July 21, 2003

<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
side by side			result set
<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
L5	L3 AND (accelerat\$3 WITH core)	1	L5
L4	L3 AND (accelerat\$3 near4 core)	0	L4
L3	L2 AND @ad<20010402	45	L3
L2	L1 AND (cod\$3 OR encod\$3)	55	L2
L1	CELP AND accelerat\$3	58	L1

END OF SEARCH HISTORY